

Day : Wednesday

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**PALM INTRANET**
**Inventor Name Search Result**

Your Search was:

Last Name = SHALEV

First Name = LEAH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09393584</a>	<a href="#">6560720</a>	150	09/09/1999	ERROR INJECTION APPARATUS AND METHOD	SHALEV, LEAH
<a href="#">09393587</a>	<a href="#">6487208</a>	150	09/09/1999	ON-LINE SWITCH DIAGNOSTICS	SHALEV, LEAH
<a href="#">09893006</a>	<a href="#">7113988</a>	150	06/28/2001	PROACTIVE ON-LINE DIAGNOSTICS IN A MANAGEABLE NETWORK	SHALEV, LEAH
<a href="#">10724960</a>	Not Issued	30	12/01/2003	Parallel TCP sender implementation	SHALEV, LEAH
<a href="#">10733630</a>	Not Issued	71	12/11/2003	Increasing TCP re-transmission process speed	SHALEV, LEAH
<a href="#">10733734</a>	Not Issued	30	12/11/2003	RDMA network interface controller with cut-through implementation for aligned DDP segments	SHALEV, LEAH
<a href="#">10747433</a>	Not Issued	30	12/29/2003	Application and verb resource management	SHALEV, LEAH
<a href="#">10904580</a>	Not Issued	30	11/17/2004	Allocation and Scaling of Descriptor Lists.	SHALEV, LEAH
<a href="#">10904979</a>	Not Issued	30	12/08/2004	SYSTEM AND METHOD FOR TRANSMITTING DATA PACKETS IN AN INFINIBAND NETWORK	SHALEV, LEAH
<a href="#">10907506</a>	Not Issued	30	04/04/2005	TCP IMPLEMENTATION WITH MESSAGE-COUNT INTERFACE	SHALEV, LEAH
<a href="#">10907507</a>	Not Issued	30	04/04/2005	CONNECTION ESTABLISHMENT ON A TCP OFFLOAD ENGINE	SHALEV, LEAH
<a href="#">10908886</a>	Not Issued	30	05/31/2005	Method For Transmitting Window Probe Packets	SHALEV, LEAH

<u>11065818</u>	Not Issued	30	02/25/2005	Method, system, and computer program product for virtual adapter destruction on a physical adapter that supports virtual adapters	SHALEV, LEAH
<u>11065821</u>	Not Issued	25	02/25/2005	Method and system for native virtualization on a partially trusted adapter using adapter bus, device and function number for identification	SHALEV, LEAH
<u>11065829</u>	Not Issued	25	02/25/2005	Data processing system, method, and computer program product for creation and initialization of a virtual adapter on a physical adapter that supports virtual adapter level virtualization	SHALEV, LEAH
<u>11065830</u>	Not Issued	30	02/25/2005	System and method for host initialization for an adapter that supports virtualization	SHALEV, LEAH
<u>11065951</u>	Not Issued	41	02/25/2005	Interrupt mechanism on an IO adapter that supports virtualization	SHALEV, LEAH
<u>11066096</u>	Not Issued	25	02/25/2005	System and method for destroying virtual resources in a logically partitioned data processing system	SHALEV, LEAH
<u>11066201</u>	Not Issued	30	02/25/2005	System and method for modification of virtual adapter resources in a logically partitioned data processing system	SHALEV, LEAH
<u>11066353</u>	Not Issued	30	02/25/2005	System, method, and computer program product for a fully trusted adapter validation of incoming memory mapped I/O operations on a physical adapter that supports virtual adapters or virtual resources	SHALEV, LEAH
<u>11066419</u>	Not Issued	30	02/25/2005	Association of memory access through protection attributes that are associated to an access control level on a PCI adapter that supports virtualization	SHALEV, LEAH
<u>11066424</u>	Not Issued	30	02/25/2005	Method, system and program product for differentiating between virtual hosts on bus transactions and associating allowable memory access for an input/output adapter	SHALEV, LEAH

				that supports virtualization	
<u>11066517</u>	Not Issued	25	02/25/2005	System and method for virtual resource initialization on a physical adapter that supports virtual resources	SHALEV, LEAH
<u>11066518</u>	Not Issued	30	02/25/2005	System and method of virtual resource modification on a physical adapter that supports virtual resources	SHALEV, LEAH
<u>11066519</u>	Not Issued	30	02/25/2005	Native virtualization on a partially trusted adapter using PCI host bus, device, and function number for identification	SHALEV, LEAH
<u>11066521</u>	Not Issued	30	02/25/2005	System and method for virtual adapter resource allocation	SHALEV, LEAH
<u>11068664</u>	Not Issued	30	02/28/2005	Method and system for fully trusted adapter validation of addresses referenced in a virtual host transfer request	SHALEV, LEAH
<u>11071553</u>	Not Issued	30	03/03/2005	Recovery from MSS change	SHALEV, LEAH
<u>11132100</u>	Not Issued	30	05/18/2005	Network acceleration architecture	SHALEV, LEAH
<u>11132101</u>	Not Issued	30	05/18/2005	Receive flow in a network acceleration architecture	SHALEV, LEAH
<u>11132853</u>	Not Issued	30	05/19/2005	Transmit flow for network acceleration architecture	SHALEV, LEAH
<u>11132854</u>	Not Issued	20	05/19/2005	Asynchronous dual-queue interface for use in network acceleration architecture	SHALEV, LEAH
<u>11163958</u>	Not Issued	20	11/04/2005	METHOD OF OFFLOADING ISCSI PDU CORRUPTION-DETECTION DIGEST GENERATION FROM A HOST PROCESSING UNIT, AND RELATED ISCSI OFFLOAD ENGINE	SHALEV, LEAH
<u>11199765</u>	Not Issued	20	08/09/2005	Remote I/O for virtualized systems	SHALEV, LEAH
<u>11223472</u>	Not Issued	30	09/09/2005	Parallel TCP without lock between sender and receiver	SHALEV, LEAH
<u>11301110</u>	Not Issued	30	12/12/2005	Memory operations in a virtualized system	SHALEV, LEAH
<u>11333028</u>	Not Issued	25	01/17/2006	Network protocol stack isolation	SHALEV, LEAH

<a href="#">11333066</a>	Not Issued	30	01/17/2006	Method and system for memory protection and security using credentials	SHALEV, LEAH
<a href="#">11333716</a>	Not Issued	25	01/17/2006	Method and system for protection and security of IO devices using credentials	SHALEV, LEAH
<a href="#">11334833</a>	Not Issued	20	01/19/2006	System and method for host-to-host communication	SHALEV, LEAH
<a href="#">11426588</a>	Not Issued	30	06/27/2006	A Method and System for Memory Address Translation and Pinning	SHALEV, LEAH
<a href="#">60214971</a>	Not Issued	159	06/29/2000	Method and apparatus for proactive on-line diagnostics of communication devices in manageable network	SHALEV, LEAH

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="SHALEV"/>	<input type="text" value="LEAH"/>	

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## Inventor Name Search Result

Your Search was:

Last Name = MAKHERVAKS

First Name = VADIM

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09772376</u>	<u>6658521</u>	150	01/30/2001	METHOD AND APPARATUS FOR ADDRESS TRANSLATION ON PCI BUS OVER INFINIBAND NETWORK	MAKHERVAKS, VADIM
<u>10724783</u>	Not Issued	30	12/01/2003	Asynchronous completion notification for an RDMA system	MAKHERVAKS, VADIM
<u>10725740</u>	Not Issued	30	12/02/2003	RDMA completion and retransmit system and method	MAKHERVAKS, VADIM
<u>10733588</u>	Not Issued	71	12/11/2003	Data transfer error checking	MAKHERVAKS, VADIM
<u>10733594</u>	Not Issued	30	12/11/2003	Reducing number of write operations relative to delivery of out-of-order RDMA send messages	MAKHERVAKS, VADIM
<u>10733630</u>	Not Issued	71	12/11/2003	Increasing TCP re-transmission process speed	MAKHERVAKS, VADIM
<u>10733668</u>	Not Issued	41	12/11/2003	Limiting number of retransmission attempts for data transfer via network interface controller	MAKHERVAKS, VADIM
<u>10733734</u>	Not Issued	30	12/11/2003	RDMA network interface controller with cut-through implementation for aligned DDP segments	MAKHERVAKS, VADIM
<u>10734037</u>	Not Issued	30	12/11/2003	In-order delivery of plurality of RDMA messages	MAKHERVAKS, VADIM
<u>10747433</u>	Not Issued	30	12/29/2003	Application and verb resource management	MAKHERVAKS, VADIM
<u>10752731</u>	Not Issued	30	01/07/2004	Completion coalescing by TCP receiver	MAKHERVAKS, VADIM

<u>10752921</u>	Not Issued	30	01/07/2004	Dynamic management of TCP reassembly buffers	MAKHERVAKS, VADIM
<u>10904580</u>	Not Issued	30	11/17/2004	Allocation and Scaling of Descriptor Lists.	MAKHERVAKS, VADIM
<u>10905810</u>	Not Issued	30	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY INITIATOR	MAKHERVAKS, VADIM
<u>10905811</u>	Not Issued	25	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY TARGET	MAKHERVAKS, VADIM
<u>10905812</u>	Not Issued	20	01/21/2005	RECOVERY FROM iSCSI CORRUPTION WITH RDMA ATP MECHANISM	MAKHERVAKS, VADIM
<u>10905813</u>	Not Issued	30	01/21/2005	iSCSI DATAMOVER INTERFACE AND FUNCTION SPLIT WITH RDMA ATP MECHANISM	MAKHERVAKS, VADIM
<u>10905814</u>	Not Issued	30	01/21/2005	DIRECT ACCESS OF SCSI BUFFER WITH RDMA ATP MECHANISM BY iSCSI TARGET AND/OR INITIATOR	MAKHERVAKS, VADIM
<u>10905815</u>	Not Issued	30	01/21/2005	SCSI BUFFER MEMORY MANAGEMENT WITH RDMA ATP MECHANISM	MAKHERVAKS, VADIM
<u>10907506</u>	Not Issued	30	04/04/2005	TCP IMPLEMENTATION WITH MESSAGE-COUNT INTERFACE	MAKHERVAKS, VADIM
<u>10907590</u>	Not Issued	30	04/07/2005	LOCKER MANAGER FOR MANAGING ACCESS TO SHARED RESOURCES	MAKHERVAKS, VADIM
<u>10908886</u>	Not Issued	30	05/31/2005	Method For Transmitting Window Probe Packets	MAKHERVAKS, VADIM
<u>11065818</u>	Not Issued	30	02/25/2005	Method, system, and computer program product for virtual adapter destruction on a physical adapter that supports virtual adapters	MAKHERVAKS, VADIM
<u>11065821</u>	Not Issued	25	02/25/2005	Method and system for native virtualization on a partially trusted adapter using adapter bus, device and function number for identification	MAKHERVAKS, VADIM
<u>11065829</u>	Not Issued	25	02/25/2005	Data processing system, method, and computer program product for creation and initialization of	MAKHERVAKS, VADIM

				a virtual adapter on a physical adapter that supports virtual adapter level virtualization	
<u>11065830</u>	Not Issued	30	02/25/2005	System and method for host initialization for an adapter that supports virtualization	MAKHERVAKS, VADIM
<u>11065951</u>	Not Issued	41	02/25/2005	Interrupt mechanism on an IO adapter that supports virtualization	MAKHERVAKS, VADIM
<u>11066096</u>	Not Issued	25	02/25/2005	System and method for destroying virtual resources in a logically partitioned data processing system	MAKHERVAKS, VADIM
<u>11066201</u>	Not Issued	30	02/25/2005	System and method for modification of virtual adapter resources in a logically partitioned data processing system	MAKHERVAKS, VADIM
<u>11066353</u>	Not Issued	30	02/25/2005	System, method, and computer program product for a fully trusted adapter validation of incoming memory mapped I/O operations on a physical adapter that supports virtual adapters or virtual resources	MAKHERVAKS, VADIM
<u>11066419</u>	Not Issued	30	02/25/2005	Association of memory access through protection attributes that are associated to an access control level on a PCI adapter that supports virtualization	MAKHERVAKS, VADIM
<u>11066424</u>	Not Issued	30	02/25/2005	Method, system and program product for differentiating between virtual hosts on bus transactions and associating allowable memory access for an input/output adapter that supports virtualization	MAKHERVAKS, VADIM
<u>11066517</u>	Not Issued	25	02/25/2005	System and method for virtual resource initialization on a physical adapter that supports virtual resources	MAKHERVAKS, VADIM
<u>11066518</u>	Not Issued	30	02/25/2005	System and method of virtual resource modification on a physical adapter that supports virtual resources	MAKHERVAKS, VADIM
<u>11066519</u>	Not Issued	30	02/25/2005	Native virtualization on a partially trusted adapter using	MAKHERVAKS, VADIM

				PCI host bus, device, and function number for identification	
<u>11066521</u>	Not Issued	30	02/25/2005	System and method for virtual adapter resource allocation	MAKHERVAKS, VADIM
<u>11068664</u>	Not Issued	30	02/28/2005	Method and system for fully trusted adapter validation of addresses referenced in a virtual host transfer request	MAKHERVAKS, VADIM
<u>11071553</u>	Not Issued	30	03/03/2005	Recovery from MSS change	MAKHERVAKS, VADIM
<u>11086713</u>	Not Issued	20	03/22/2005	System and method for efficient implementation of a shared receive queue	MAKHERVAKS, VADIM
<u>11132100</u>	Not Issued	30	05/18/2005	Network acceleration architecture	MAKHERVAKS, VADIM
<u>11132101</u>	Not Issued	30	05/18/2005	Receive flow in a network acceleration architecture	MAKHERVAKS, VADIM
<u>11132853</u>	Not Issued	30	05/19/2005	Transmit flow for network acceleration architecture	MAKHERVAKS, VADIM
<u>11132854</u>	Not Issued	20	05/19/2005	Asynchronous dual-queue interface for use in network acceleration architecture	MAKHERVAKS, VADIM
<u>11163958</u>	Not Issued	20	11/04/2005	METHOD OF OFFLOADING ISCSI PDU CORRUPTION-DETECTION DIGEST GENERATION FROM A HOST PROCESSING UNIT, AND RELATED ISCSI OFFLOAD ENGINE	MAKHERVAKS, VADIM
<u>11199765</u>	Not Issued	20	08/09/2005	Remote I/O for virtualized systems	MAKHERVAKS, VADIM
<u>11217196</u>	Not Issued	30	09/01/2005	Method of offloading iSCSI TCP/IP processing from a host processing unit, and related iSCSI TCP/IP offload engine	MAKHERVAKS, VADIM
<u>11301110</u>	Not Issued	30	12/12/2005	Memory operations in a virtualized system	MAKHERVAKS, VADIM
<u>11333028</u>	Not Issued	25	01/17/2006	Network protocol stack isolation	MAKHERVAKS, VADIM
<u>11357445</u>	Not Issued	25	02/17/2006	Apparatus and method for out-of-order placement and in-order completion reporting or remote direct memory access operations	MAKHERVAKS, VADIM G.



<u>11357449</u>	Not Issued	30	02/17/2006	Apparatus and method for stateless CRC calculation	MAKHERVAKS, VADIM G.
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PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = MACHULSKY

First Name = ZORIK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10724783</a>	Not Issued	30	12/01/2003	Asynchronous completion notification for an RDMA system	MACHULSKY, ZORIK
<a href="#">10725740</a>	Not Issued	30	12/02/2003	RDMA completion and retransmit system and method	MACHULSKY, ZORIK
<a href="#">10733588</a>	Not Issued	71	12/11/2003	Data transfer error checking	MACHULSKY, ZORIK
<a href="#">10733594</a>	Not Issued	30	12/11/2003	Reducing number of write operations relative to delivery of out-of-order RDMA send messages	MACHULSKY, ZORIK
<a href="#">10733630</a>	Not Issued	71	12/11/2003	Increasing TCP re-transmission process speed	MACHULSKY, ZORIK
<a href="#">10733668</a>	Not Issued	41	12/11/2003	Limiting number of retransmission attempts for data transfer via network interface controller	MACHULSKY, ZORIK
<a href="#">10733734</a>	Not Issued	30	12/11/2003	RDMA network interface controller with cut-through implementation for aligned DDP segments	MACHULSKY, ZORIK
<a href="#">10734037</a>	Not Issued	30	12/11/2003	In-order delivery of plurality of RDMA messages	MACHULSKY, ZORIK
<a href="#">10747433</a>	Not Issued	30	12/29/2003	Application and verb resource management	MACHULSKY, ZORIK
<a href="#">10905810</a>	Not Issued	30	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY INITIATOR	MACHULSKY, ZORIK
<a href="#">10905811</a>	Not Issued	25	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY TARGET	MACHULSKY, ZORIK
<a href="#">10905812</a>	Not	20	01/21/2005	RECOVERY FROM iSCSI	MACHULSKY,

	Issued			CORRUPTION WITH RDMA ATP MECHANISM	ZORIK
<u>10905813</u>	Not Issued	30	01/21/2005	iSCSI DATAMOVER INTERFACE AND FUNCTION SPLIT WITH RDMA ATP MECHANISM	MACHULSKY, ZORIK
<u>10905814</u>	Not Issued	30	01/21/2005	DIRECT ACCESS OF SCSI BUFFER WITH RDMA ATP MECHANISM BY iSCSI TARGET AND/OR INITIATOR	MACHULSKY, ZORIK
<u>10905815</u>	Not Issued	30	01/21/2005	SCSI BUFFER MEMORY MANAGEMENT WITH RDMA ATP MECHANISM	MACHULSKY, ZORIK
<u>10907590</u>	Not Issued	30	04/07/2005	LOCKER MANAGER FOR MANAGING ACCESS TO SHARED RESOURCES	MACHULSKY, ZORIK
<u>11086713</u>	Not Issued	20	03/22/2005	System and method for efficient implementation of a shared receive queue	MACHULSKY, ZORIK
<u>11132100</u>	Not Issued	30	05/18/2005	Network acceleration architecture	MACHULSKY, ZORIK
<u>11132101</u>	Not Issued	30	05/18/2005	Receive flow in a network acceleration architecture	MACHULSKY, ZORIK
<u>11132853</u>	Not Issued	30	05/19/2005	Transmit flow for network acceleration architecture	MACHULSKY, ZORIK
<u>11132854</u>	Not Issued	20	05/19/2005	Asynchronous dual-queue interface for use in network acceleration architecture	MACHULSKY, ZORIK
<u>11199765</u>	Not Issued	20	08/09/2005	Remote I/O for virtualized systems	MACHULSKY, ZORIK
<u>11333028</u>	Not Issued	25	01/17/2006	Network protocol stack isolation	MACHULSKY, ZORIK
<u>11333066</u>	Not Issued	30	01/17/2006	Method and system for memory protection and security using credentials	MACHULSKY, ZORIK
<u>11333716</u>	Not Issued	25	01/17/2006	Method and system for protection and security of IO devices using credentials	MACHULSKY, ZORIK
<u>11334833</u>	Not Issued	20	01/19/2006	System and method for host-to-host communication	MACHULSKY, ZORIK
<u>11426588</u>	Not Issued	30	06/27/2006	A Method and System for Memory Address Translation and Pinning	MACHULSKY, ZORIK

## Inventor Search Completed: No Records to Display.

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PALM INTRANET

**Inventor Name Search Result**

Your Search was:

Last Name = BIRAN

First Name = GIORA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09406527</a>	<a href="#">6345347</a>	150	09/27/1999	ADDRESS PROTECTION USING A HARDWARE-DEFINED APPLICATION KEY	BIRAN, GIORA
<a href="#">09591216</a>	<a href="#">6829647</a>	150	06/09/2000	SCALEABLE HARDWARE ARBITER	BIRAN, GIORA
<a href="#">09772376</a>	<a href="#">6658521</a>	150	01/30/2001	METHOD AND APPARATUS FOR ADDRESS TRANSLATION ON PCI BUS OVER INFINIBAND NETWORK	BIRAN, GIORA
<a href="#">09773053</a>	Not Issued	161	01/31/2001	Data communications interfaces	BIRAN, GIORA
<a href="#">09810940</a>	<a href="#">7050437</a>	150	03/16/2001	WIRE SPEED REASSEMBLY OF DATA FRAMES	BIRAN, GIORA
<a href="#">09810941</a>	<a href="#">6947430</a>	150	03/16/2001	NETWORK ADAPTER WITH EMBEDDED DEEP PACKET PROCESSING	BIRAN, GIORA
<a href="#">10470366</a>	Not Issued	30	12/22/2003	Providing control information to a management processor of a communications switch	BIRAN, GIORA
<a href="#">10619960</a>	Not Issued	41	07/15/2003	Controlling flow of data between data processing systems via a memory	BIRAN, GIORA
<a href="#">10619988</a>	Not Issued	41	07/15/2003	Controlling flow of data between data processing systems via a memory	BIRAN, GIORA
<a href="#">10619989</a>	Not Issued	71	07/15/2003	TRANSFERRING INTERRUPTS FROM A PERIPHERAL DEVICE TO A HOST COMPUTER SYSTEM	BIRAN, GIORA
<a href="#">10724783</a>	Not Issued	30	12/01/2003	Asynchronous completion notification for an RDMA system	BIRAN, GIORA
<a href="#">10724960</a>	Not	30	12/01/2003	Parallel TCP sender	BIRAN, GIORA

	Issued			implementation	
<u>10725740</u>	Not Issued	30	12/02/2003	RDMA completion and retransmit system and method	BIRAN, GIORA
<u>10733588</u>	Not Issued	71	12/11/2003	Data transfer error checking	BIRAN, GIORA
<u>10733594</u>	Not Issued	30	12/11/2003	Reducing number of write operations relative to delivery of out-of-order RDMA send messages	BIRAN, GIORA
<u>10733630</u>	Not Issued	71	12/11/2003	Increasing TCP re-transmission process speed	BIRAN, GIORA
<u>10733734</u>	Not Issued	30	12/11/2003	RDMA network interface controller with cut-through implementation for aligned DDP segments	BIRAN, GIORA
<u>10734037</u>	Not Issued	30	12/11/2003	In-order delivery of plurality of RDMA messages	BIRAN, GIORA
<u>10747433</u>	Not Issued	30	12/29/2003	Application and verb resource management	BIRAN, GIORA
<u>10752731</u>	Not Issued	30	01/07/2004	Completion coalescing by TCP receiver	BIRAN, GIORA
<u>10752921</u>	Not Issued	30	01/07/2004	Dynamic management of TCP reassembly buffers	BIRAN, GIORA
<u>10905810</u>	Not Issued	30	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY INITIATOR	BIRAN, GIORA
<u>10905811</u>	Not Issued	25	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY TARGET	BIRAN, GIORA
<u>10905812</u>	Not Issued	20	01/21/2005	RECOVERY FROM iSCSI CORRUPTION WITH RDMA ATP MECHANISM	BIRAN, GIORA
<u>10905813</u>	Not Issued	30	01/21/2005	iSCSI DATAMOVER INTERFACE AND FUNCTION SPLIT WITH RDMA ATP MECHANISM	BIRAN, GIORA
<u>10905814</u>	Not Issued	30	01/21/2005	DIRECT ACCESS OF SCSI BUFFER WITH RDMA ATP MECHANISM BY iSCSI TARGET AND/OR INITIATOR	BIRAN, GIORA
<u>10905815</u>	Not Issued	30	01/21/2005	SCSI BUFFER MEMORY MANAGEMENT WITH RDMA ATP MECHANISM	BIRAN, GIORA
<u>10907507</u>	Not Issued	30	04/04/2005	CONNECTION ESTABLISHMENT ON A TCP	BIRAN, GIORA

				OFFLOAD ENGINE	
<u>10908886</u>	Not Issued	30	05/31/2005	Method For Transmitting Window Probe Packets	BIRAN, GIORA
<u>11064568</u>	Not Issued	30	02/24/2005	Computer system bus bridge	BIRAN, GIORA
<u>11064745</u>	Not Issued	41	02/24/2005	Computer system architecture	BIRAN, GIORA
<u>11065818</u>	Not Issued	30	02/25/2005	Method, system, and computer program product for virtual adapter destruction on a physical adapter that supports virtual adapters	BIRAN, GIORA
<u>11065821</u>	Not Issued	25	02/25/2005	Method and system for native virtualization on a partially trusted adapter using adapter bus, device and function number for identification	BIRAN, GIORA
<u>11065829</u>	Not Issued	25	02/25/2005	Data processing system, method, and computer program product for creation and initialization of a virtual adapter on a physical adapter that supports virtual adapter level virtualization	BIRAN, GIORA
<u>11065830</u>	Not Issued	30	02/25/2005	System and method for host initialization for an adapter that supports virtualization	BIRAN, GIORA
<u>11065951</u>	Not Issued	41	02/25/2005	Interrupt mechanism on an IO adapter that supports virtualization	BIRAN, GIORA
<u>11066096</u>	Not Issued	25	02/25/2005	System and method for destroying virtual resources in a logically partitioned data processing system	BIRAN, GIORA
<u>11066201</u>	Not Issued	30	02/25/2005	System and method for modification of virtual adapter resources in a logically partitioned data processing system	BIRAN, GIORA
<u>11066353</u>	Not Issued	30	02/25/2005	System, method, and computer program product for a fully trusted adapter validation of incoming memory mapped I/O operations on a physical adapter that supports virtual adapters or virtual resources	BIRAN, GIORA
<u>11066419</u>	Not Issued	30	02/25/2005	Association of memory access through protection attributes that	BIRAN, GIORA

				are associated to an access control level on a PCI adapter that supports virtualization	
<a href="#">11066424</a>	Not Issued	30	02/25/2005	Method, system and program product for differentiating between virtual hosts on bus transactions and associating allowable memory access for an input/output adapter that supports virtualization	BIRAN, GIORA
<a href="#">11066517</a>	Not Issued	25	02/25/2005	System and method for virtual resource initialization on a physical adapter that supports virtual resources	BIRAN, GIORA
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